

**Amendments to the Claims**

The following listing of claims will replace all prior versions, and listings, of claims in the ancestor application.

1. (Currently Amended) A processor, comprising:  
a decoder to implement a first flow synonym and a second and second flow synonym for a first instruction, said decoder to select at least one of said first and second flow ~~synonym~~ synonyms for decoding said first instruction; and  
a scheduler to schedule said selected at least one of said first and second flow ~~synonym~~ synonyms for execution.
2. (Original) The processor of claim 1, wherein said first flow synonym is to execute on a first execution unit and said second flow synonym is to execute on a second execution unit.
3. (Original) The processor of claim 2, wherein said first execution unit and said second execution unit are of differing types.
4. (Original) The processor of claim 2, wherein said first execution unit and said second execution unit are of differing precisions.
5. (Original) The processor of claim 1, wherein said first flow synonym and said second flow synonym are to execute on a common execution unit.

6. (Previously Presented) The processor of claim 1, wherein said decoder is to make said selection based upon processor status.

7. (Previously Presented) The processor of claim 1, wherein said decoder is to make said selection based upon a rule.

8. (Original) The processor of claim 7, wherein said rule considers a power configuration of said processor.

9. (Original) The processor of claim 7, wherein said rule considers failure status of an execution unit.

10. (Cancelled)

11. (Previously Presented) The processor of claim 1, further comprising a retirement module to retire whichever said first or said second flow synonym first completes execution, if both said first and second flow synonyms are scheduled for execution.

12. (Currently Amended) The processor of claim 1, further comprising a retirement module to compare execution results of said first and said second ~~flow synonym~~ flow synonyms and raise an exception upon mismatch, if both said first and second flow synonyms are scheduled for execution by different execution units.

13. (Original) A method, comprising:

determining whether each of a plurality of execution units is available; and

if a first one of said plurality of execution units is available, then choosing a first flow synonym corresponding to said first available execution unit and further scheduling said first flow synonym for execution on said first available execution unit.

14. (Original) The method of claim 13, further comprising if none of said plurality of execution units is available, then arbitrating to select a second flow synonym from a plurality of flow synonyms.

15. (Currently Amended) The method of claim 13, further comprising if more than one of said plurality of execution ~~units is~~ units are available, then choosing said first flow synonym from a plurality of flow synonyms corresponding to one of said available execution units based upon system performance rules.

16. (Original) The method of claim 13, wherein said determining includes reading a processor status register.

17. (Original) The method of claim 16, wherein said processor status register indicates a second one of said execution units is less available due to a fault in said second one of said execution units.

18. (Original) The method of claim 16, wherein said processor status register indicates a second one of said execution units is available due to a processor reduced power mode.

19. (Currently Amended) A method, comprising:  
decoding an instruction into a first flow synonym and a second flow synonym;  
scheduling said first flow synonym and said second flow synonym for execution on a first execution unit and a second execution unit, respectively;  
executing said first flow synonym on said first execution unit; and  
executing said second flow synonym on said second execution unit.

20. (Original) The method of claim 19, further comprising retiring said first flow synonym when said first flow synonym finishes execution before said second flow synonym finishes execution.

21. (Previously Presented) The method of claim 19, further comprising raising an exception when a first execution result of said first flow synonym does not equal a second execution result of said second flow synonym.

22. (Currently Amended) A system, comprising:

a processor including a decoder to implement a first flow synonym and a second ~~and second~~ flow synonym for a first instruction, said decoder to select at least one of said first and second flow ~~synonym~~ synonyms for decoding said first instruction, and a scheduler to schedule said selected at least one of said first and second flow ~~synonym~~ synonyms for execution;

an interface to couple said processor to input/output circuitry;  
and

an audio input/output circuitry coupled to said interface.

23. (Original) The system of claim 22, wherein said first flow synonym is to execute on a first execution unit and said second flow synonym is to execute on a second execution unit.

24. (Original) The system of claim 23, wherein said first execution unit and said second execution unit are of differing types.

25. (Original) The system of claim 23, wherein said first execution unit and said second execution unit are of differing precisions.

26. (Original) The system of claim 22, wherein said first flow synonym and said second flow synonym are to execute on a common execution unit.

27. (Previously Presented) The system of claim 22, wherein said decoder is to make said selection based upon processor status.

28. (Previously Presented) The system of claim 22, wherein said decoder is to make said selection based upon a rule.

29. (Original) The system of claim 28, wherein said rule considers a power configuration of said processor.

30. (Original) The system of claim 28, wherein said rule considers failure status of an execution unit.

31. (Cancelled)

32. (Previously Presented) The system of claim 22, further comprising a retirement module to retire whichever said first or said second flow synonym first completes execution, if both said first and second flow synonyms are scheduled for execution.

33. (Currently Amended) The system of claim 22, further comprising a retirement module to compare execution results of said first and said second flow ~~synonym~~ synonyms and raise an exception upon mismatch, if both said first and second flow synonyms are scheduled for execution by different execution units.

34. (Original) A processor, comprising:  
means for determining whether each of a plurality of execution units is available; and  
if said means for determining determines that a first one of said plurality of execution units is available, then means for choosing a first

flow synonym corresponding to said first available execution unit and further means for scheduling said first flow synonym for execution on said first available execution unit.

35. (Original) The processor of claim 34, further comprising if said means for determining determines that none of said plurality of execution units is available, then means for arbitrating to select a second flow synonym from a plurality of flow synonyms.

36. (Currently Amended) The processor of claim 34, further comprising if said means for determining determines that more than one of said plurality of execution units ~~is~~ units are available, then means for choosing said first flow synonym from a plurality of flow synonyms corresponding to one of said available execution units based upon system performance rules.

37. (Original) The processor of claim 34, wherein said means for determining includes means for reading a processor status register.

38. (Original) The processor of claim 37, wherein said processor status register indicates a second one of said execution units is less available due to a fault in said second one of said execution units.

39. (Original) The processor of claim 37, wherein said processor status register indicates a second one of said execution units is available due to a processor reduced power mode.

40. (Currently Amended) A processor, comprising:

means for decoding an instruction into a first flow synonym and a second flow synonym;

means for scheduling said first flow synonym and said second flow synonym for execution on a first execution unit and a second execution unit, respectively;

means for executing said first flow synonym on said first execution unit; and

means for executing said second flow synonym on said second execution unit.

41. (Original) The processor of claim 40, further comprising

means for retiring said first flow synonym when said first flow synonym finishes execution before said second flow synonym finishes execution.

42. (Original) The processor of claim 40, further comprising means for raising an exception when a first execution result of said first flow synonym does not equal a second execution result of said second flow synonym.